

HT1117 Series 1A General Purpose LDO

Features

- Output voltage ranges: Fixed range of 1.8V, 2.5V, 2.85V, 3.3V, 5.0V or adjustable type.
- High accuracy: ±2%
- Low voltage drop: 1.1V (typ.), V_{OUT}=5.0V at 1A
- Applications
- Active SCSI terminations
- Post regulator for switching power supplies
- · Low voltage microcontrollers

- Guaranteed output current: 1A
- Low quiescent current: 8mA (typ.)
- Integrated current limit & thermal protection circuits
- SOT-223 package
- Motherboard clock supplies
- Battery chargers

General Description

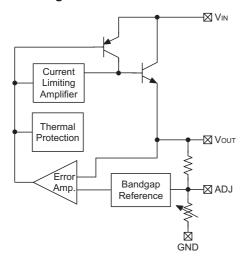
The HT1117 devices are a series of three-terminal high current low voltage regulators. They can deliver an output current of 1A and can accept input voltages up to 12V. The devices are available in both adjustable and fixed output voltage type with a range of 1.8V to 5.0V. Internal current limit and thermal protection circuits provide protection against overload conditions that could create excessive junction temperatures.

Selection Table

Part No.	Output Voltage	Package	Marking
HT1117-ADJ	Adjust		
HT1117-18	1.8V		HT1117-ADJ
HT1117-25	2.5V	SOT-223	HT1117-18 HT1117-25
HT1117-28	2.85V		HT1117-28
HT1117-33	3.3V		HT1117-33 HT1117-50
HT1117-50	5.0V		

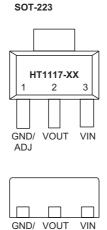
Note: For lead free devices, a "#" mark is suffixed at the end of the date code.

Block Diagram





Pin Assignment



GND/ VOUT ADJ

Absolute Maximum Ratings*

Input Supply VoltageV_SS-0.3V to V_SS+13V

Storage Temperature-50°C to 125°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

"*" Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. The guaranteed specifications apply only for the test conditions listed.

Recommended Operating Conditions

Input Supply VoltageV_{SS}=0.3V to V_{SS}+12V

Ambient Temperature-40°C to 85°C

Thermal Information

Symbol	Parameter	Package	Max.	Unit
θ_{JA}	Thermal Resistance (Junction to Ambient)	SOT-223	134	°C/W
PD	Power Dissipation	SOT-223	0.75	W

Note: P_D is measured at Ta= 25°C



Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{REF}	Reference Voltage	$2.75V \le V_{IN} \le 12V$	1.225	1.250	1.275	V
		HT1117-18	1.764	1.800	1.836	V
		HT1117-25	2.45	2.50	2.55	V
V _{OUT}	Output Voltage	HT1117-28	2.793	2.850	2.907	V
		HT1117-33	3.234	3.300	3.366	V
		HT1117-50	4.90	5.00	5.10	V
ΔV_{LOAD}	Load Regulation (see note 2)	$10mA \le I_{OUT} \le 1A$	1		30	mV
ΔV_{LINE}	Line Regulation	$2.75V \le V_{IN} \le 12V$	_	0.015	0.150	%/V
V _{DIF}	Dropout Voltage (see note 3)	ΔV _{OUT} =2%, I _{OUT} =1A		1.1	1.3	V
I _{LIMIT}	Current Limit (see note 4)	∆V _{OUT} =10%	1	2.3		А
I _{ADJ}	Adjust Pin Current (Variable Version)	$2.75V \le V_{IN} \le 12V$		55	120	μΑ
I _{SS}	Quiescent Current (Fixed Version)	$2.75V \le V_{IN} \le 12V$		8	13	mA
RR	Ripple Rejection	120Hz input ripple C _{OUT} =22μF		60		dB
<u>ΔVout</u> ΔTa	Temperature Coefficient	-40°C <ta<85°c< td=""><td></td><td>±0.4</td><td></td><td>mV/°C</td></ta<85°c<>		±0.4		mV/°C

Electrical Characteristics

 $T_J=25^{\circ}C$, $V_{IN}=V_{OUT}+1.5V$, $I_O=10mA$, unless otherwise specified (see note 1)

Note: 1. Specifications are production tested at room temperature, Ta. Specifications within the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

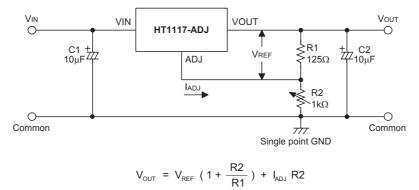
- 2. Load regulation is measured at a constant junction temperature, using pulse testing with a low ON time and is guaranteed up to the maximum power dissipation. Power dissipation is determined by the input/output differential voltage and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range. The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} Ta) / \theta_{JA}.$
- 3. Dropout voltage is defined as the input voltage minus the output voltage that produces a 2% change in the output voltage from the value at $V_{IN} = V_{OUT}$ +1.5V with a fixed load.
- 4. Current limit is measured by pulsing for a short time.



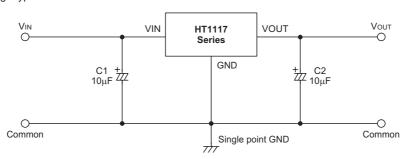
Application Circuits

Basic Circuits

• Variable voltage type



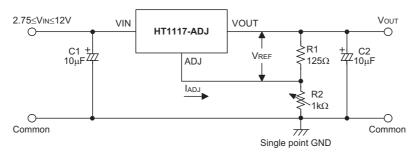
- Note: C1 is required if the needed if the device is located far from filter capacitors, the recommended value is 10μ F. C2 is required for stability, the recommended value is 10μ F. R1 is required for regulation, the recommended value is 125Ω .
- Fixed voltage type



Note: C1 is required if the needed if the device is located far from filter capacitors, the recommended value is 10μ F. C2 is required for stability, the recommended value is 10μ F.

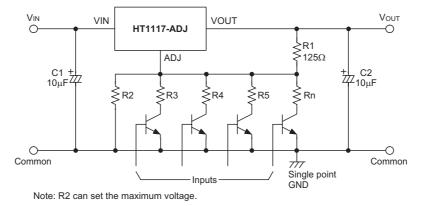
Typical Application Circuits

• 1.25V~10.5V regulator

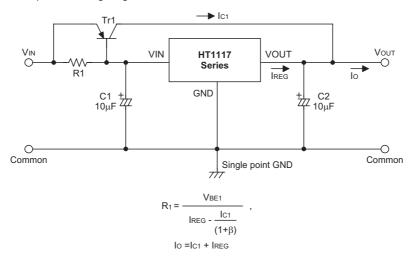




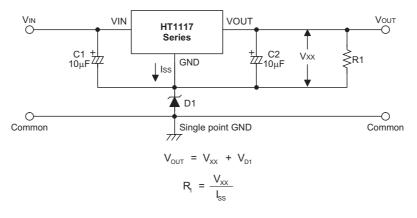
• Digitally selected outputs



• High output current positive voltage regulator

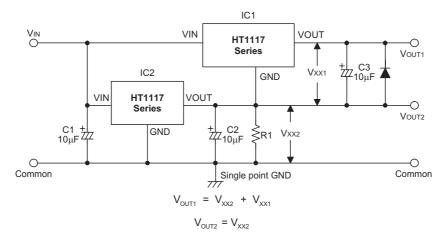


• Increased Output voltage Circuit

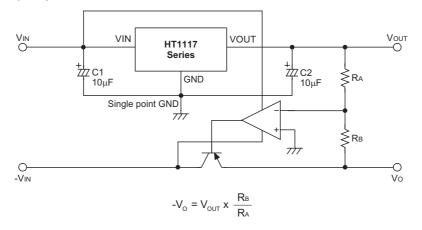




• Dual Supply Circuit



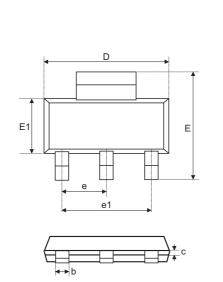
• Tracking Voltage Regulator

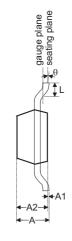




Package Information

3-pin SOT-223 Outline Dimensions



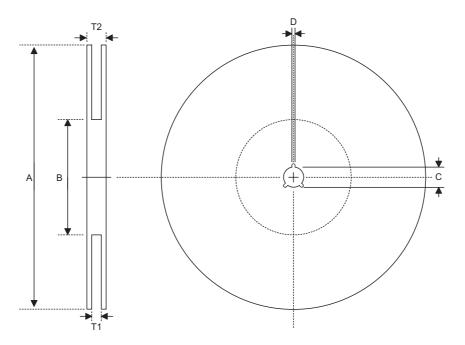


Symbol	Dimensions in mm			
Symbol	Min.	Nom.	Max.	
А		—	1.8	
A1	0.02	—	0.10	
A2	1.50	—	1.70	
b	0.66		0.84	
С	0.23		0.35	
D	6.30		6.70	
E	6.70		7.30	
E1	3.30		3.70	
е		2.3	_	
e1		4.6	_	
L	0.75	_	_	
θ	0°		10°	



Product Tape and Reel Specifications

Reel Dimensions

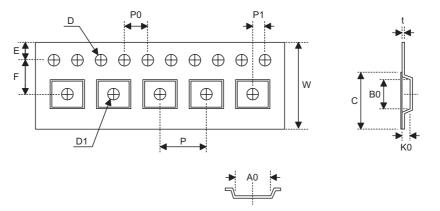


SOT-223

Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	330±1
В	Reel Inner Diameter	62±1.5
С	Spindle Hole Diameter	12.75±0.15
D	Key Slit Width	2+0.6
T1	Space Between Flange	12.4+0.2
T2	Reel Thickness	16.4–0.4



Carrier Tape Dimensions



SOT-223

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	12±0.3
Р	Cavity Pitch	8±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	5.5±0.05
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.1
P0	Perforation Pitch	4±0.1
P1	Cavity to Perforation (Length Direction)	2±0.05
A0	Cavity Length	6.9±0.1
B0	Cavity Width	7.5±0.1
K0	Cavity Depth	2.1±0.1
t	Carrier Tape Thickness	0.3±0.05
С	Cover Tape Width	9.3



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